

FIG. 3



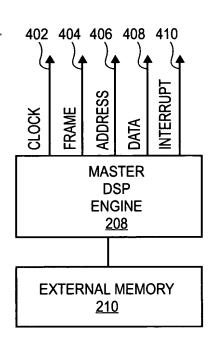


FIG. 4

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PLD

स्र

Fpulse DSPM

cookie

isp/serial eprom link

PCM STREAM C PCM STREAM A

25M

OSC

+3.3

decoding 6
registers

Jukebox Reset

misc control

TOP AND BOTTOM STACKING SM CONNECTORS

2.048M

SuperSync

PCM STREAM B
PCM STREAM D

2



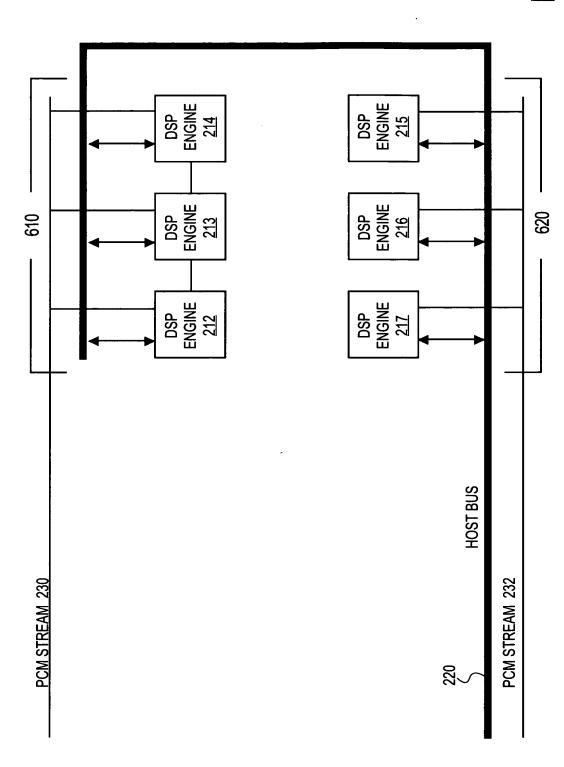
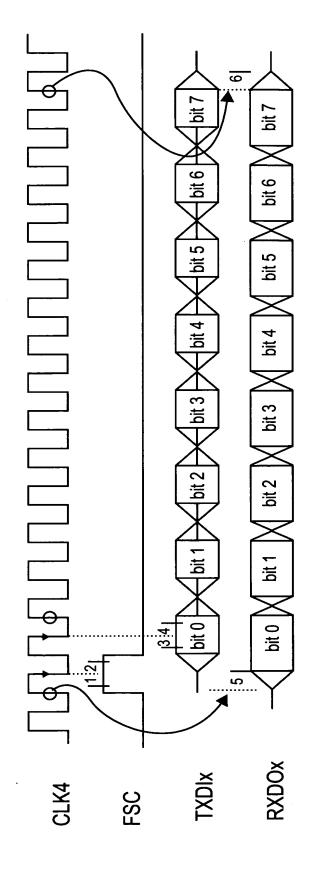


FIG. 6





↓ = fsc and txd input sampling active edge

O = rxd output active edge